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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/388,857 ✓
Filing Date September 1, 1999 ✓
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L. Schillinger
Attorney's Docket No. MI22-878
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

EV1 8265 6867

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

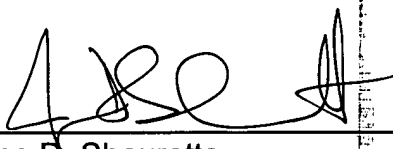
References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the references listed on the attached Form PTO-1449. No admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 12/4/02


James D. Shaurette
Reg. No. 39,833
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828
(509) 624-4276

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/388,857
	Filing Date	September 1, 1999
	First Named Inventor	Luan C. Tran
	Group Art Unit	2813
	Examiner Name	L. Schillinger
Total Number of Pages in This Submission	Attorney Docket Number	MI22-878

ENCLOSURES (check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	James D. Shaurette, Reg. No. 39,833 Wells St. John, P.S.
Signature	
Date	12/4/02

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